



#5 G. L. 2816
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

4/26/02

In re application of

Katsuji KIMURA

Appln. No.: 09/940,472

Confirmation No.: 4891

Group Art Unit: 2816

Filed: August 29, 2001

Examiner: Minh T. NGUYEN

For: LINEAR VOLTAGE SUBTRACTOR/ADDER
CIRCUIT AND MOS DIFFERENTIAL
AMPLIFIER CIRCUIT THEREFOR

RECEIVED
APR 23 2002
TECHNOLOGY CENTER 2800

RESPONSE TO RESTRICTION REQUIREMENT

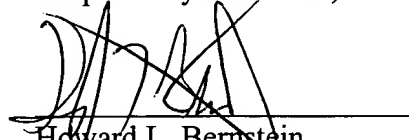
Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated March 26, 2002 in which the Examiner has required a restriction, Applicant responds by electing Group I, *i.e.*, claims 1-4, drawn to voltage subtractor/adder circuits, classified in class 327, subclass 361. This election is made without prejudice to the filing of a divisional application directed to claims 5-13.

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

SUGHRUE MION, PLLC
2100 Pennsylvania Avenue, N.W.
Washington, D.C. 20037-3213
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

Date: April 22, 2002